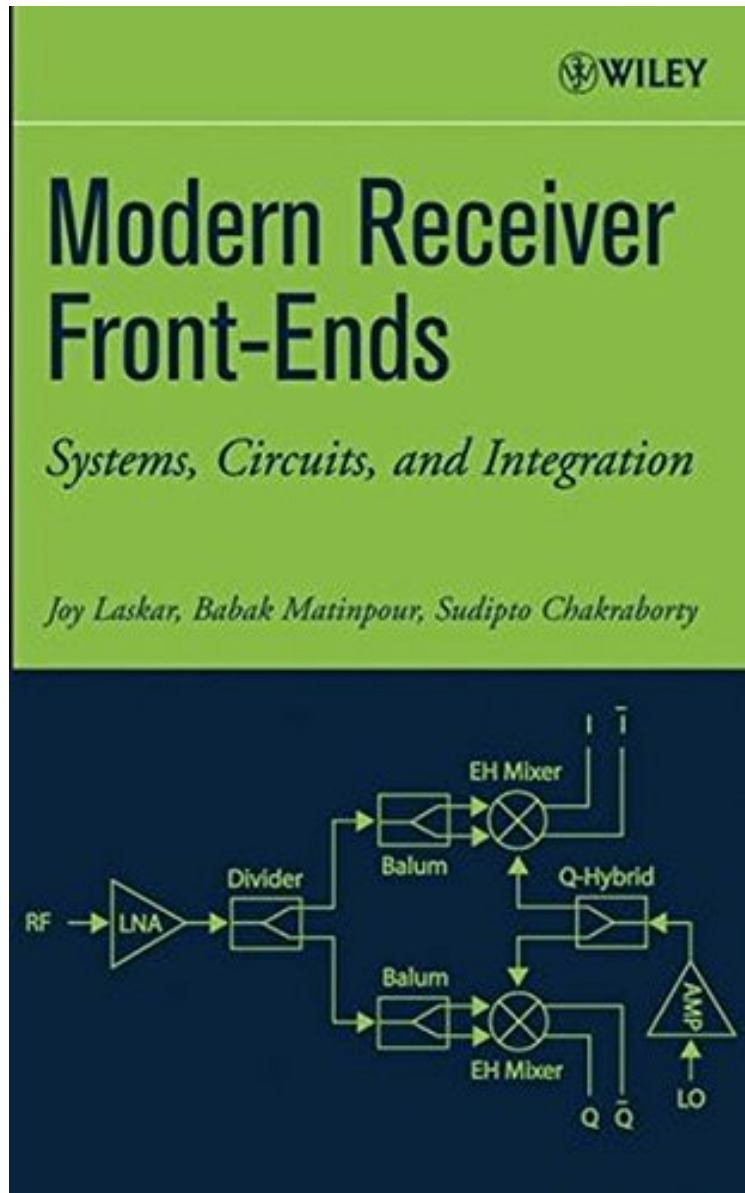




Joy Laskar, Babak Matinpour, Sudipto Chakraborty
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Modern Receiver Front-Ends: Systems, Circuits, and Integration

Joy Laskar, Babak Matinpour, Sudipto Chakraborty : Modern Receiver Front-Ends: Systems, Circuits, and Integration before purchasing it in order to gage whether or not it would be worth my time, and all praised Modern Receiver Front-Ends: Systems, Circuits, and Integration:

7 of 8 people found the following review helpful. Disappointingly poor quality textbookBy AmpereI purchased this

text for a class taught by Joy Laskar at Georgia Tech. I was very disappointed with the last of polish the book had. The author, Laskar, is very knowledgeable about his field, however the text he provides lacks most of the nuance you would expect from a textbook. Many portions read more like excerpts from published papers, complete with the lack of in depth examples and explanations, and while references are plentiful, it would be preferable for a student to have the example or worked out problems mentioned present in the text instead of located in a trade journal somewhere which one may or may not have access to. Additionally, the images throughout the book were horrible. Lacking color, and resized from originals in a sloppy way, most are pixelated, grayscale, and difficult to use.

Architectures BABAK MATINPOUR and JOY LASKAR * Describes the actual implementation of receiver architectures from the initial design to an IC-based product * Presents many tricks-of-the-trade not usually covered in textbooks * Covers a range of practical issues including semiconductor technology selection, cost versus performance, yield, packaging, prototype development, testing, and analysis * Discusses architectures that are employed in modern broadband wireless systems

"Such a book will be helpful for the understanding of receiver front-end development and architectural trade-offs." (Microwave Journal, September 2004) "The well-written text is illustrated with numerous references. (Choice, June 2004, Vol. 41 No. 10) From the Back Cover A practical, hands-on guidebook to advanced receiver design Radio-frequency integrated circuit (RF IC) design is one of the most important fields in modern technology, as advances in chip architecture are essential to delivering the full promise of broadband wireless communications. Modern Receiver Front-Ends outlines today's most cutting-edge approaches to advanced receiver design, covering topics ranging from system design to circuits and integration of a full solution. Bridging the gap between analytical understanding of receiver IC design and the limitations associated with practical implementation, the text offers a breadth of coverage second to none. An ideal resource for entry-level designers and students of circuit design, the book addresses: The actual implementation of wireless systems A range of practical issues associated with receiver design, including semiconductor technology selection, cost versus performance, yield, prototype development, testing, and analysis The architectures employed in modern broadband wireless systems The fundamental challenges in receiver design, from IC implementation to packaging A wealth of tricks-of-the-trade and practical considerations Preparing students for the real world of RF IC design and introducing them to the multidisciplinary challenges to be encountered in the real world of IC design, Modern Receiver Front-Ends offers an ideal bridge from university courses to standard industry practice. About the Author JOY LASKAR, PhD, is the Joseph M. Pettit Professor of Electrical and Computer Engineering at the Georgia Institute of Technology. He is the author of over 200 technical papers and has fifteen patents pending. Professor Laskar has also received numerous awards for his work, including the Army Research Office's Young Investigator Award, the National Science Foundation's CAREER Award, and the IEEE Rappaport Award. He is Director of the Georgia Electronic Design Center (GEDC). BABAK MATINPOUR, PhD, is the cofounder and vice president of VT Silicon Inc., a fables wireless start-up developing next generation Ics for 3G mobile phones. Dr. Matinpour has published over twenty IEEE papers and presented several invited talks on GaAs and SiGe RF ICs. SUDIPTO CHAKRABORTY, PhD, is a senior research engineer in the School of Electrical and Computer Engineering at the Georgia Institute of Technology, Atlanta, Georgia, where he is involved in analysis, design, and characterization of advanced wired and wireless circuits and systems towards advanced communication technologies using silicon-based processes. Dr. Chakraborty has authored or coauthored over thirty technical articles, presented several invited talks in the area of integrated transceiver design, and has two patents pending.